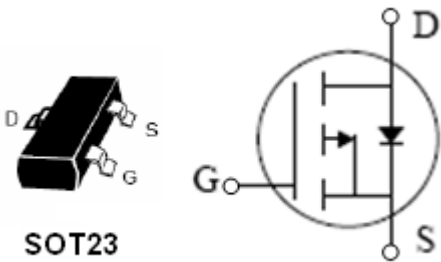


P-Channel Enhancement Mode  
Field Effect Transistor

CE2301

- ▼ Simple drive requirement
- ▼ Small package outline
- ▼ Surface mount device
- ▼ Pb-Free package is available



■ Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	-20	V
VGS	Gate-Source Voltage	±8	V
ID@TA=25℃	Continuous Drain Current3, VGS @ -4.5V	-2.3	A
IDM	Pulsed Drain Current1,2	-8	A
PD@TA=25℃	Total Power Dissipation	0.9	W
TSTG	Storage Temperature Range	-55 to 150	℃
TJ	Operating Junction Temperature Range	-55 to 150	℃

■ Thermal Data

Symbol	Parameter	Value	Unit
Rthj-a	Thermal Resistance Junction-ambient3 Max.	140	℃/W

■ **Electrical Characteristics@ $T_j=25^{\circ}\text{C}$ (unless otherwise specified)**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BVDSS	Drain-Source Breakdown Voltage	VGS=0V, ID=-250uA	-20	-	-	V
RDS(ON)	Static Drain-Source On-Resistance <sup>2</sup>	VGS=-4.5V, ID=-2.8A	-	69	100	mΩ
		VGS=-2.5V, ID=-2.0A	-	83	150	mΩ
VGS(th)	Gate Threshold Voltage	VDS=VGS, ID=-250uA	-0.45	-	-0.95	V
gfs	Forward Transconductance	VDS=-5V, ID=-4.0A	-	6.5	-	S
IDSS	Drain-Source Leakage Current (Tj=25oC)	VDS=20V, VGS=0V	-	-	-1	uA
IGSS	Gate-Source Leakage	VGS=±8V, VDS=0V	-	-	±100	nA
Qg	Total Gate Charge <sup>2</sup>	ID=-2.8A	-	15.23	-	nC
Qgs	Gate-Source Charge	VDS=-6V	-	5.49	-	nC
Qgd	Gate-Drain ("Miller") Charge	VGS=-4.5V	-	2.74	-	nC
td(on)	Turn-on Delay Time <sup>2</sup>	VDS=-6V	-	17.28	-	ns
tr	Rise Time	ID=-1A	-	3.73	-	ns
td(off)	Turn-off Delay Time	RG=6Ω, VGS=-4.5V	-	36.05	-	ns
tf	Fall Time	RD=6Ω	-	6.19	-	ns
Ciss	Input Capacitance	VGS=0V	-	882.51	-	pF
Coss	Output Capacitance	VDS=-6V	-	145.54	-	pF
Crss	Reverse Transfer Capacitance	f=1.0MHz	-	97.26	-	pF

■ **Source-Drain Diode**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
IS	Continuous Source Current ( Body Diode )		-	-	-2.4	A
VSD	Forward On Voltage <sup>2</sup>	IS=-0.75A, VGS=0V	-	-0.8	-1.2	V

**Notes:**

1. Pulse width limited by Max. junction temperature.
2. Pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$ .
3. Surface mounted on 1 in<sup>2</sup> copper pad of FR4 board ; 270°C/W when mounted on min. copper pad.

TYPICAL ELECTRICAL CHARACTERISTICS

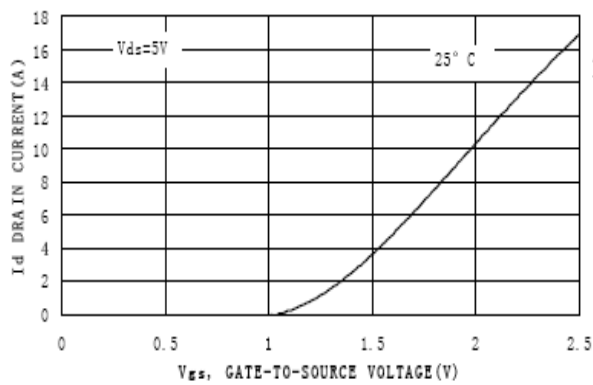


Figure 1. Transfer Characteristics

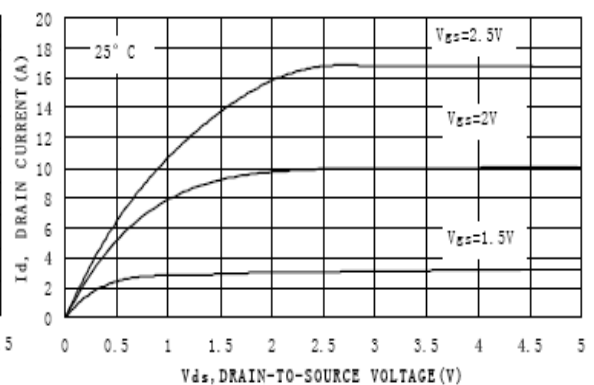


Figure 2. On-Region Characteristics

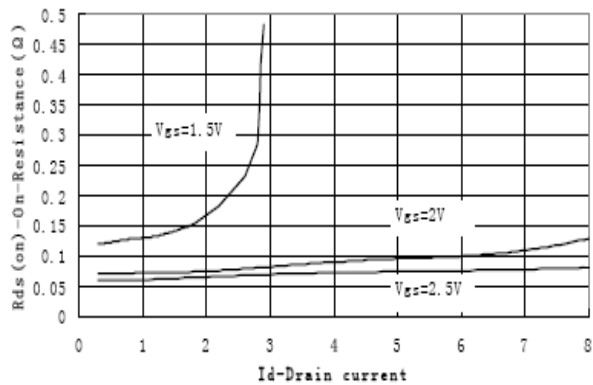


Figure 3. On-Resistance versus Drain Current

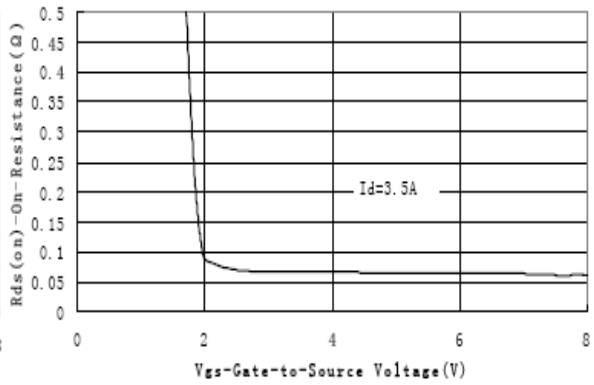


Figure 4. On-Resistance vs. Gate-to-Source Voltage

TYPICAL ELECTRICAL CHARACTERISTICS

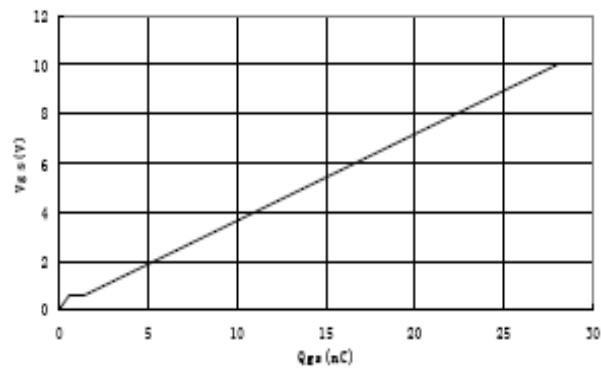


Figure 5. Gate Charge

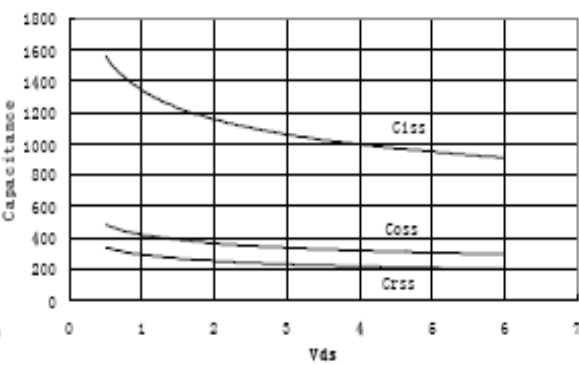


Figure 6. Capacitance

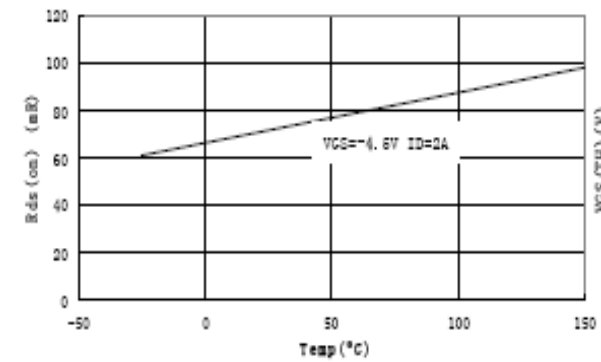


Figure 7. On-Resistance Vs. Junction Temperature

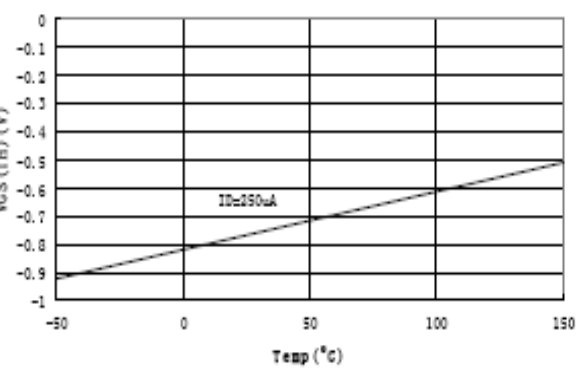
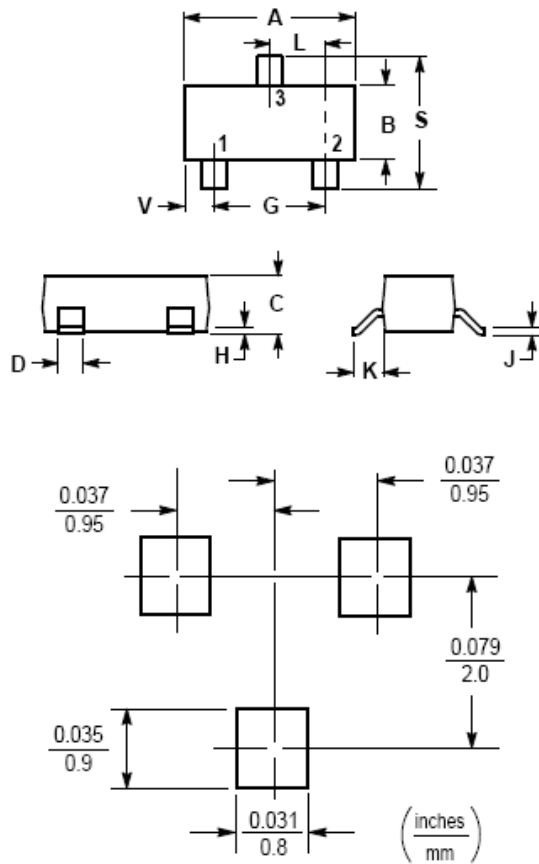


Figure 8.  $V_{th}$  Vs. Junction Temperature

## SOT-23



## NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M,1982
2. CONTROLLING DIMENSION: INCH.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.1102	0.1197	2.80	3.04
B	0.0472	0.0551	1.20	1.40
C	0.0350	0.0440	0.89	1.11
D	0.0150	0.0200	0.37	0.50
G	0.0701	0.0807	1.78	2.04
H	0.0005	0.0040	0.013	0.100
J	0.0034	0.0070	0.085	0.177
K	0.0140	0.0285	0.35	0.69
L	0.0350	0.0401	0.89	1.02
S	0.0830	0.1039	2.10	2.64
V	0.0177	0.0236	0.45	0.60